

## **In The Specification**

Please amend the specification as follows:

On page 1, line 1, delete "SPECIFICATION".

On page 1, line 5, delete in its entirety and insert therefor --- **1. Field of the Invention** ---.

On page 1, line 12, delete in its entirety and insert therefor --- **2. Discussion of the Background Art** ---.

On page 17, line 1, delete in its entirety

On page 18, line 22, delete in its entirety and insert therefor --- **SUMMARY OF THE INVENTION** ---.

On page 21, line 1, delete in its entirety and insert therefor --- **BRIEF DESCRIPTION OF THE DRAWINGS**

Fig. 1 is a block diagram showing one example of a stabilized power supply circuit.

Fig. 2 is a circuit diagram showing one example of a stabilized power supply circuit.

Fig. 3 is a diagram showing one example of the source voltage characteristics with respect to the output voltage in a conventional stabilized power supply circuit.

Fig. 4 is a diagram whose scale is expanded 10,000 times expanded Fig. 3.

Fig. 5 is a diagram showing the output gain phase-frequency characteristics of a conventional stabilized power supply circuit.

Fig. 6 shows the PSRR characteristics of a conventional stabilized power supply circuit.

Fig. 7 is a circuit diagram showing the first embodiment of the present invention.

Fig. 8 is a circuit diagram showing a variation of the first embodiment of the present invention.

Fig. 9 shows the source voltage dependency of the voltage in each section of the circuit in Fig. 16.

Fig. 10 shows the canceling operation as to the PSRR characteristics of the present invention.

Fig. 11 shows an example of the reference voltage generation circuit.

Fig. 12 shows the operation of the canceling signal generation circuit.

Fig. 13 is an example of the canceling signal generation circuit.

Fig. 14 is a graph showing the working of the canceling signal generation circuit.

Fig. 15 is a circuit diagram showing the second embodiment of the present invention.

Fig. 16 is a circuit diagram showing the third embodiment of the present invention.

Fig. 17 is a circuit diagram showing a variation of the third embodiment of the present invention.

Fig. 18 is a block diagram showing the first embodiment of the present invention.

Fig. 19 is a block diagram showing the second embodiment of the present invention.

Fig. 20 is a block diagram showing the third embodiment of the present invention.

Fig. 21 is a diagram for explaining the canceling operation of the present invention.

Fig. 22 is another diagram for explaining the canceling operation.---

On page 5, between lines 2 and 3, insert

**DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT ---.**

On page 35, lines 23-31, delete in their entirety.

On page 36, lines 1-31, delete in their entirety.

On page 37, lines 1-31, delete in their entirety.

On page 38, lines 1-6, delete in their entirety.